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S&H Form: (10/01)

## REPLY/AMENDMENT FEE TRANSMITTAL

Attorney Docket No.	1614.1082
Application Number	09/671,117
Filing Date	September 28, 2000
First Named Inventor	Hideo MIYAKE et al.
Group Art Unit	2187

RECEIVED

AMOUNT ENCLOSED	484.00	Examiner Name	Hong Chong Kim	OCT 21 2002
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### FEE CALCULATION (fees effective 10/01/01)

Technology Center 2100

CLAIMS AS AMENDED	Claims Remaining After Amendment	Highest Number Previously Paid For	Number Extra	Rate	Calculations
TOTAL CLAIMS	18	- 20 =	0	X \$ 18.00 =	\$ 0.00
INDEPENDENT CLAIMS	8	- 7 =	1	X \$ 84.00 =	84.00

Since an Official Action set an original due date of May 10, 2002, petition is hereby made for an extension to cover the date this reply is filed for which the requisite fee is enclosed (1 month (\$110); 2 months (\$400); 3 months (\$920); 4 months (\$1,440); 5 months (\$1,960)):

\$400

If Notice of Appeal is enclosed, add (\$320)

If Statutory Disclaimer under Rule 20(d) is enclosed, add fee (\$110)

Total of above Calculations =

\$ 484.00

Reduction by 50% for filing by small entity (37 CFR 1.9, 1.27 & 1.28)

TOTAL FEES DUE = \$ 484.00

(1) If entry (1) is less than entry (2), entry (3) is "0".

(2) If entry (2) is less than 20, change entry (2) to "20".

(4) If entry (4) is less than entry (5), entry (6) is "0".

(5) If entry (5) is less than 3, change entry (5) to "3".

### METHOD OF PAYMENT

- Check enclosed as payment.
- Charge "TOTAL FEES DUE" to the Deposit Account No. below.
- No payment is enclosed and no charges to the Deposit Account are authorized at this time (unless specifically required to obtain a filing date).

### GENERAL AUTHORIZATION

- If the above-noted "AMOUNT ENCLOSED" is not correct, the Commissioner is hereby authorized to credit any overpayment or charge any additional fees necessary to:

Deposit Account No. 19-3935

Deposit Account Name STAAS & HALSEY LLP

- The Commissioner is also authorized to credit any overpayments or charge any additional fees required under 37 CFR 1.16 (filing fees) or 37 CFR 1.17 (processing fees) during the prosecution of this application, including any related application(s) claiming benefit hereof pursuant to 35 USC § 120 (e.g., continuations/divisionals/CIPs under 37 CFR 1.53(b) and/or continuations/divisionals/CPAs under 37 CFR 1.53(d)) to maintain pendency hereof or of any such related application.

SUBMITTED BY: STAAS & HALSEY LLP

Typed Name	JON MUSKIN	Reg. No.	2200 43,824
Signature		Date	10/10/02

CERTIFICATE UNDER 37 CFR 2.101 Staas & Halsey LLP

I hereby certify that this correspondence is being deposited with the United States Postal Service at first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231

on 10/10/02  
STAA\$ & HALSEY

By:   
Date: 10/10/02



Docket No.: 1614.1082

9/10  
JPK  
10-22-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hideo MIYAKE et al.

Serial No. 09/671,117

Group Art Unit: 2187

Confirmation No. 8617

Filed: September 28, 2000

Examiner: Hong Chong Kim

For: METHOD OF CONTROLLING A CACHE MEMORY TO INCREASE AN ACCESS SPEED TO A MAIN MEMORY, AND A COMPUTER USING THE METHOD

AMENDMENT

RECEIVED

Assistant Commissioner for Patents  
Washington, D.C. 20231

OCT 21 2002

Sir:

Technology Center 2100

This is in response to the Office Action mailed May 10, 2002, and having a period for response set to expire on August 10, 2002. A Petition for a Two-Month Extension of Time is submitted, extending the period for response to October 10, 2002.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.

IN THE CLAIMS:

Please REPLACE claims 1, 4, 13, 15, 17-20 and ADD new claims 18-20 in accordance with the following:

1. (ONCE AMENDED) A method of controlling a cache memory connected to a main memory and divided into a plurality of cache blocks, comprising :

supplying a lock signal generated on the basis of a lock instruction to the cache memory to set a replace-inhibition state of at least one of the cache blocks in which replacing said at least one of the cache blocks to the main memory is inhibited ; and

performing either reading or writing of the main memory by using the remaining cache

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